

PCN Number:	20180522001	PCN Date:	May 29, 2018
Title:	Datasheet for TPS543C20		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



TPS543C20

SLUSCD4B –MARCH 2017–REVISED MAY 2018

Changes from Revision A (September 2017) to Revision B

Page

• Changed "16 Vin" and "16 V" input voltage to "14 Vin" and "14 V"	1
• Changed <i>Absolute Maximum Ratings</i> VIN row MAX from "20" to "16"; add VIN to SW row	5
• Added <i>Absolute Maximum Ratings</i> new footnote 1; delete "VIN < 2-ms transient" row	5
• Changed <i>Absolute Maximum Ratings</i> VDD row MAX from "22" to "16"	5
• Changed <i>Absolute Maximum Ratings</i> "SW" rows to "SW to PGND" and "< 10 ns" MAX from "23" to "20"	5
• Changed <i>Recommended Operating Conditions</i> VIN maximum from "16" to "14" V; added VIN to SW specs	6
• Changed <i>Recommended Operating Conditions</i> BOOT maximum from "19.5" to "23.5" V	6
• Added in <i>Recommended Operating Conditions</i> " to PGND" after SW row; changed DC maximum from "16" to "18" V and < 10 ns from "21" to "18" V; added new note 1	6
• Changed <i>Electrical Characteristics</i> INPUT SUPPLY and CURRENT Power stage voltage MAX value from "16" to "14" and VDD supply voltage MAX value from "22" to "16" V	7
• Added statement re: mandatory requirement for VIN to GND capacitor	15
• Added sentence after " is valid for VDD ≥ 5 V."	21
• Changed Table 6 Input voltage MAX from "16" to "14 V"; add new footnote 1	25
• Changed Table 6 Line regulation TEST CONDITION from "5 V ≤ V _{IN} ≤ 16 V" to "5 V ≤ V _{IN} ≤ 14 V"	25
• Added "Place a 10-nF to 100-nF capacitor close to IC from Pin 25 VIN to Pin 27 GND." to <i>Layout Guidelines</i>	33
• Changed Condition for Figure 43 from "V _{OUT} = 1 V" to "V _{OUT} = 5 V" and Figure 44 from "V _{OUT} = 5 V" to "V _{OUT} = 1 V"	35

The datasheet number will be changing.

Device Family	Change From:	Change To:
TPS543C20	SLUSCD4A	SLUSCD4B

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/TPS543C20>

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:

TPS543C20RVFR	TPS543C20RVFT		
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For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
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