

PCN Number:	20200129001	PCN Date:	Mar. 18, 2020
Title:	Datasheet for LMK04228		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
		<input type="checkbox"/>	Wafer Bump Site
		<input type="checkbox"/>	Wafer Bump Material
		<input type="checkbox"/>	Wafer Bump Process
		<input type="checkbox"/>	Wafer Fab Site
		<input type="checkbox"/>	Wafer Fab Materials
		<input type="checkbox"/>	Wafer Fab Process

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



LMK04228

SNAS689A – OCTOBER 2017 – REVISED JULY 2019

Changes from Original (October 2017) to Revision A	Page
• Changed the data sheet release status from custom to catalog	1
• Deleted reference to distribution mode (unsupported)	1
• Deleted reference to dynamic delay (unsupported)	1
• Updated default output table note	11
• Added missing cross reference to differential voltage definition	12
• Removed typical phase noise plots	13
• Updated description for improved clarity	17
• Deleted reference to distribution mode (unsupported)	17
• Updated delay circuit descriptions for improved clarity	18
• Deleted reference to dynamic delay (unsupported)	19
• Deleted reference to dynamic delay, bypass mode in clock output block diagram (unsupported)	21
• Deleted reference to distribution mode in SYNC/SYSREF clocking path diagram (unsupported)	22
• Clarified digital lock detect for cases where phase detector frequency exceeds default PLL1_WND_SIZE	29
• Removed device functional modes section	32
• Clarified requirements for unused registers in recommended programming sequence	32
• Added registers 0x171 and 0x172 to default register programming	32
• Deleted redundant user-inaccessible registers in register map	33
• Changed address bits to clarify address position relative to data bits	33
• Deleted references to dynamic delay in register map (unsupported)	33
• Corrected CLKInX_R register size in register map	35
• Corrected PLL1_N register size in register map	35
• Deleted reference to DCLKoutX_MUX bypass mode (unsupported)	40
• Corrected delay value descriptions for SDCLKoutY_ADLY	41
• Deleted reference to dynamic delay (unsupported)	42

• Updated missing cross-reference.....	49
• Corrected CLKInX_R register length.....	59
• Corrected PLL1_N register length.....	60
• Corrected PLL2_R register length.....	64
• Split PLL2_FCAL_DIS and PLL2_N register tables into separate definitions.....	66
• Added register 0x171 and 0x172 to register descriptions.....	72
• Corrected RB_PLL1_LD and RB_PLL2_LD polarity.....	73
• Added note clarifying PLL1_WND_SIZE and impact on holdover exit.....	75
• Changed references to deprecated software tools to point to TICS Pro.....	78
• Removed application curves section.....	79
• Deleted unused column in typical current consumption table.....	80
• Fixed truncated layout example image.....	82
• Deleted links to deprecated software tools.....	83

The datasheet number will be changing.

Device Family	Change From:	Change To:
LMK04228	SNAS689	SNAS689A

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/LMK04228>

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:

LMK04228NKDR	LMK04228NKDT		
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For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
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